

1 Inventor: **Brent Keeth et al.**
2 Title: **Semiconductor Memory Circuitry**
3 Assignee: **Micron Technology, Inc.**
4 Serial No.: **08/530,661**
5 Filing Date: **September 20, 1995**

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7 **SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

8 References -- See Attached Form PTO-1449

9 The attached form PTO-1449 is submitted in compliance with
10 37 CFR §1.56.

11 Respectfully submitted,

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13 Dated: 6/20/96

14 Attorney: 
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